

# Systems Engineering DSIG

- **Tuesday, June 15, 2021, 1/2 day, 40+ attendees**

## Highlights from this Meeting:

### **SysML v2 Update & Demo – S Friedenthal, E Seidewitz, M Bajaj**

- Language updates (interactions, stakeholder & concerns, teature paths & dot notation, enhanced individual modeling)
- API updates (Updates to branching, OSLC PSM and pilot implementation, and conformance testing)
- Revised submission for August, 2021

### **Building a digital thread with SysML v2 - M Bajaj and Anya Sharma, Intercax**

- Digital thread is the connection between different kinds of models and data repositories across the lifecycle
- Example: SysML v2 requirements and architecture, JIRA issues, HW Bill of Materials, SW components, SW build artifacts
- Enables change impact assessment, synchronization, report generation
- Syndeia captures digital thread as a graph
- Manages inter-model links

# Systems Engineering DSIG

- **Highlights from this Meeting (cont.):**

## **Integrated SE-ModSim for Advanced Driver Assist Systems - Chris Davey**

- Growing complexity of autonomous electric vehicles
- Ensure safety, comfort, and predictability in a complex environment
- MBSE integrated with Mod Sim and advanced visualization essential to manage complexity and provide agile response to new features

## **Key Parameter Development & Management (KPD&M) – Rob Hendrickson**

- US Army Armament Center approach to systems engineering
- Understand parameters that impact performance
- Understand noise factors
- Optimize design for robustness
- Success and challenges with MBSE