

# Schedulability Analysis with the UML Profile for Modeling and Analysis of Real-Time and Embedded Systems



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# Topics

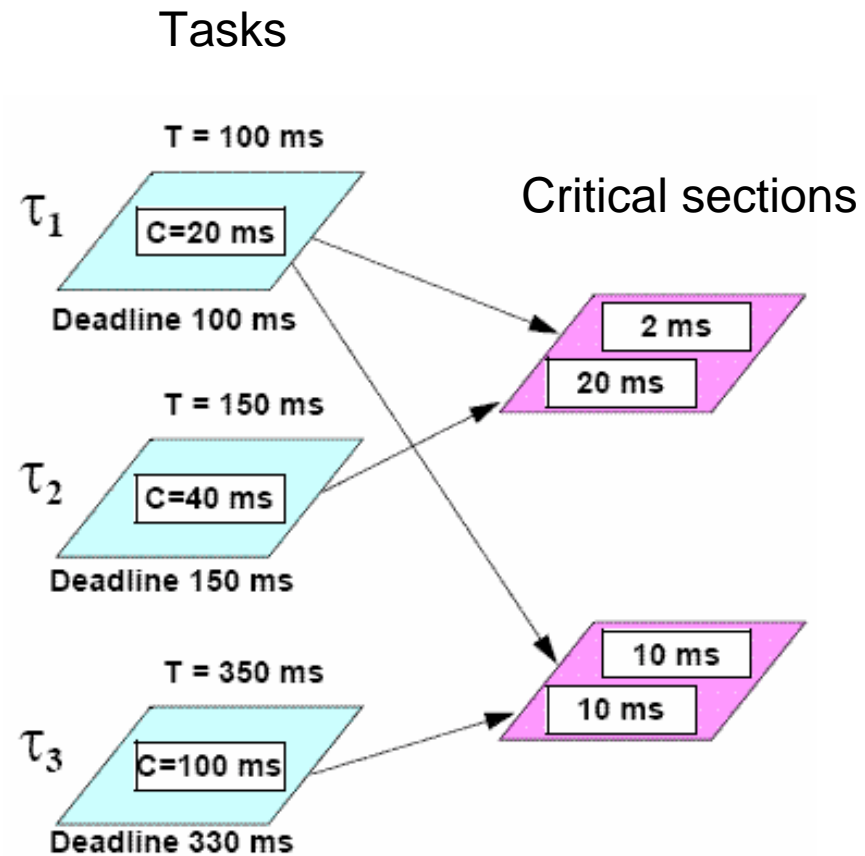
- ❑ A flash of schedulability analysis:
  - ❑ RMA: foundations
  - ❑ Offset-based techniques: high level models.
- ❑ Structural and behavioral modeling elements in MARTE.
- ❑ Examples of the notation used.

# Options for managing time

- ❑ Compile-time schedules:
  - ❑ Known as cyclic executives
  - ❑ Predictability through static schedule
  - ❑ Logical integrity often compromised by timing structure
  - ❑ Difficult to maintain, even with model based strategies
- ❑ Run-time schedules:
  - ❑ Priority-based schedulers
  - ❑ Preemptive or non preemptive
  - ❑ Fixed priority or dynamic priority (deadline based i.e.)
  - ❑ Analytical methods needed for predictability
  - ❑ Separates logical structure from timing
- ❑ Server-based frameworks:
  - ❑ Combine static or dynamic schedules with budgets and periods enforced at run-time

→ RMA

# A simple example



# Why Are Deadlines Missed?

- ❑ For a task to meet its deadline, it must accommodate
  - ❑ preemption: time waiting for higher-priority tasks
  - ❑ execution: time to do its own work
  - ❑ blocking: time delayed by lower-priority tasks
- ❑ The task is schedulable if the sum of its preemption, execution, and blocking is less than its deadline for the worst possible case:
  - ❑ Execution is unavoidable (unless requirements change).
  - ❑ Preemption can be minimized by choosing an optimum priority assignment
  - ❑ Main focus: identify and reduce blocking

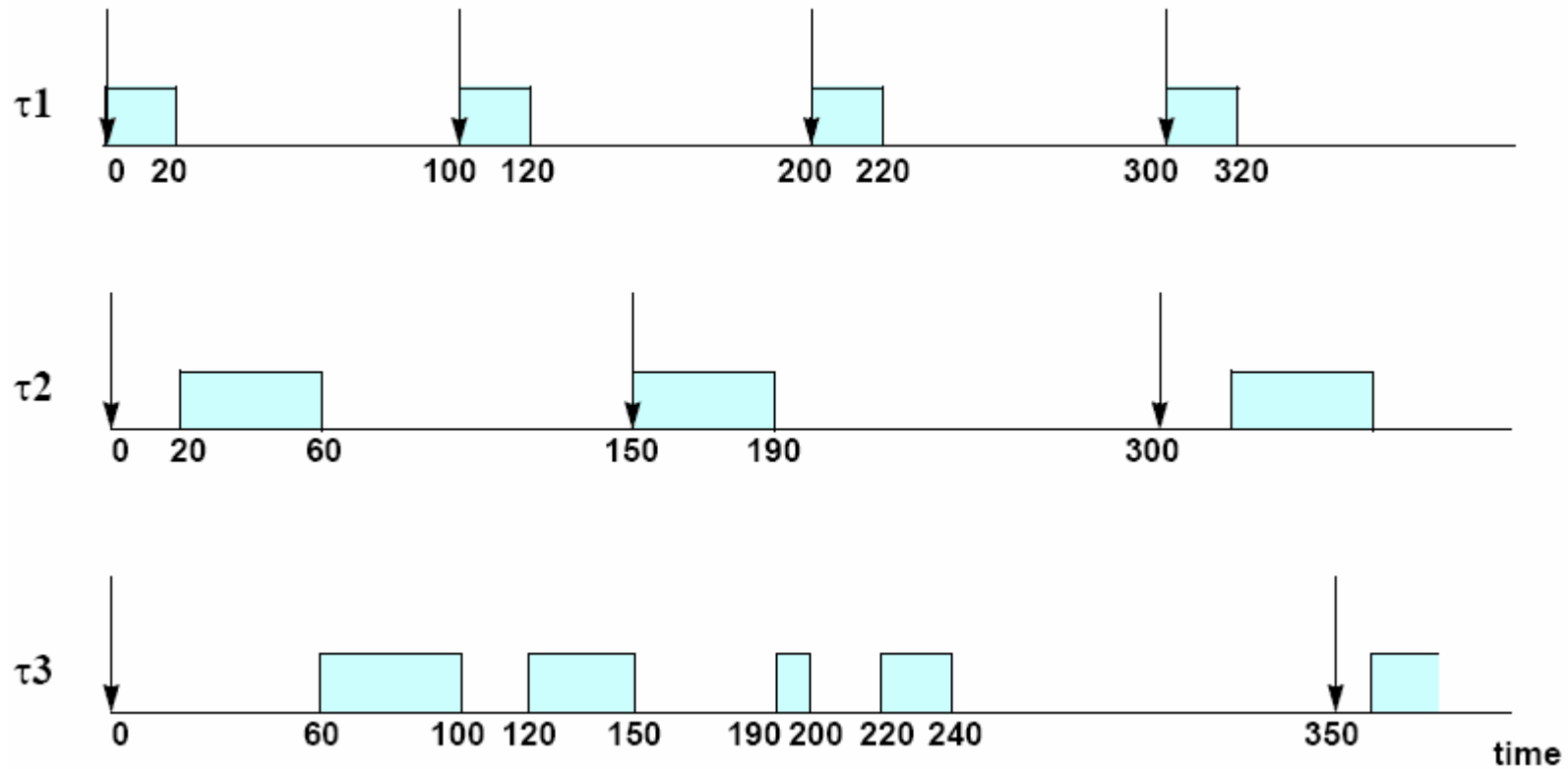
# Concepts and Definitions (periodic tasks)

- ❑ Periodic task
  - ❑ initiated at fixed intervals
  - ❑ must finish before start of next cycle
- ❑ Task's CPU utilization:  $U_i = C_i / T_i$ 
  - ❑  $C_i$  = compute time (execution time) for task  $t_i$
  - ❑  $T_i$  = period of task  $t_i$
  - ❑  $P_i$  = priority of task  $t_i$
  - ❑  $D_i$  = deadline of task  $t_i$
  - ❑  $f_i$  = phase of task  $t_i$
  - ❑  $R_i$  = response time of task  $t_i$
- ❑ CPU utilization for a set of tasks:  $U = U_1 + U_2 + \dots + U_n$

# Basic Principles of RMA

- ❑ Two concepts help to build the worst-case condition:
  - ❑ Critical instant. The worst-case response time for all tasks in the task set is obtained when all tasks are activated at the same time
  - ❑ Checking the first deadline. When all tasks are activated at the same time, if a task meets its first deadline, it will always meet all of its deadlines
- ❑ Based on these concepts, several results arise:
  - ❑ Optimality of rate monotonic priorities
  - ❑ Utilization bound test
  - ❑ Exact test

# Critical instant





# Utilization and Response Time tests

- Utilization Bound(UB) Test: A set of  $n$  independent periodic tasks, with deadlines at the end of the periods, scheduled by the rate monotonic algorithm will always meet its deadlines, for all task phasings, if:  $U \leq U(n) = n(2^{1/n} - 1)$
- Completion time test:
  - For a number the tasks according to priority (highest priority= $t_1$ , lowest priority= $t_n$ )
  - Under a critical instant condition, the amount of work  $W_i(t)$  of tasks at priority  $P_i$  or higher started before  $t$  is

$$W_i(t) = \left\lceil \frac{t}{T_1} \right\rceil C_1 + \dots + \left\lceil \frac{t}{T_{i-1}} \right\rceil C_{i-1} + C_i$$

# Response time evaluation

- $R_i$  may be computed by the following iterative formula:

$$a_0 = C_1 + C_2 + \dots + C_i$$

$$a_{k+1} = W_i(a_k) = \left\lceil \frac{a_k}{T_1} \right\rceil C_1 + \dots + \left\lceil \frac{a_k}{T_{i-1}} \right\rceil C_{i-1} + C_i$$

- The iteration ends when:

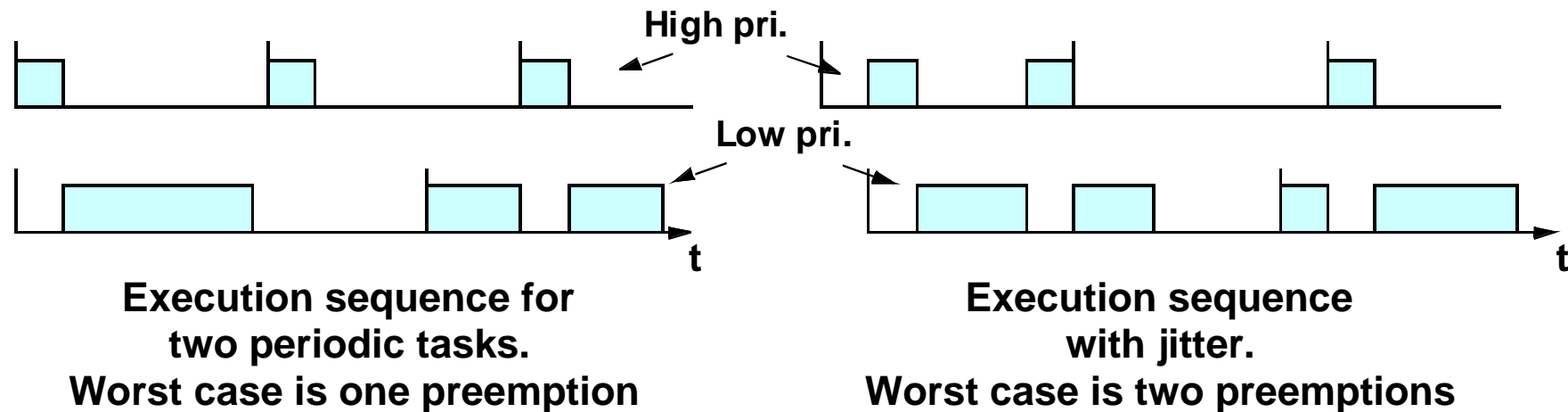
$$a_{k+1} = a_k = R_i$$

- Task  $t_i$  is schedulable if:

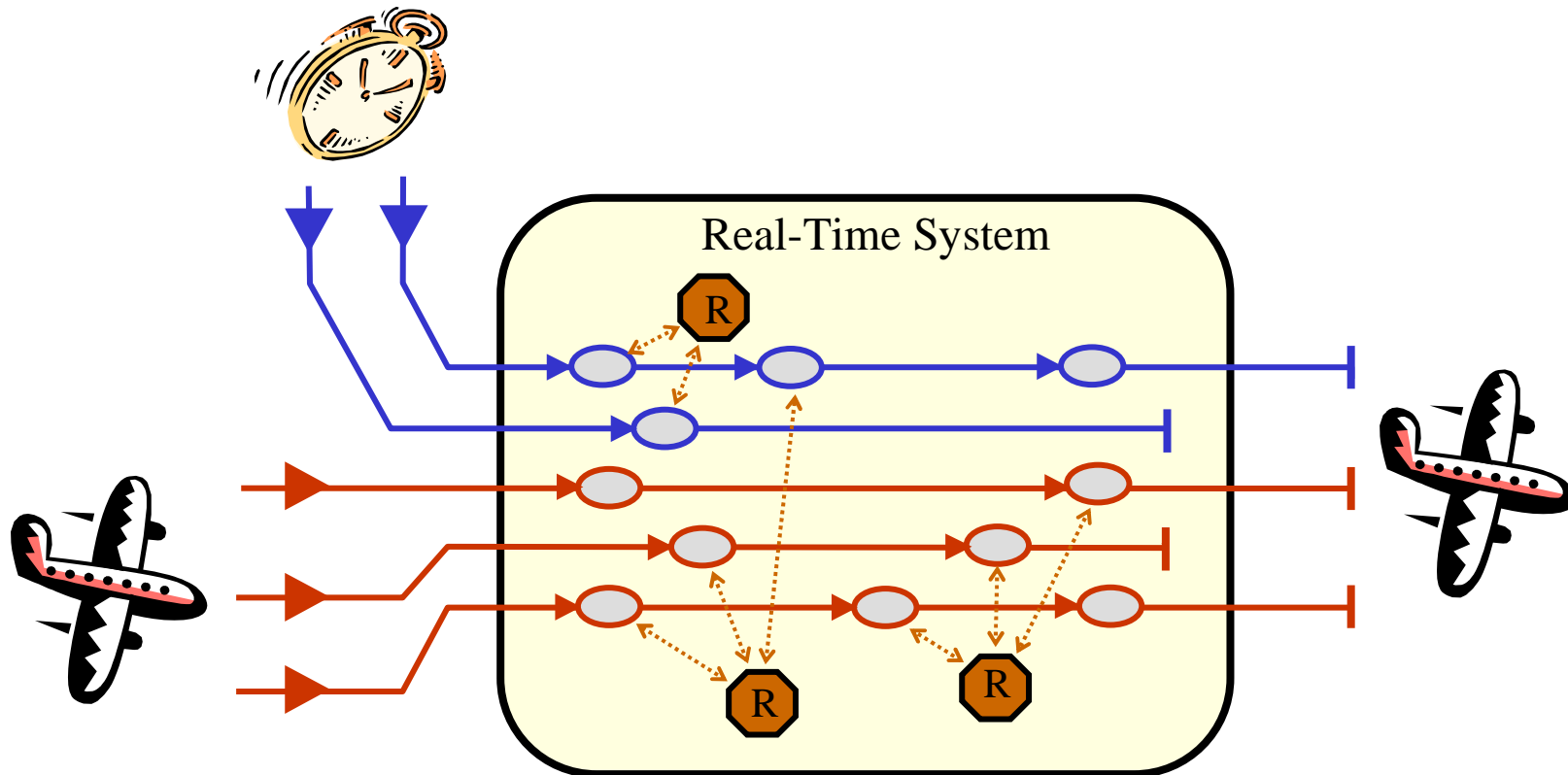
$$R_i \leq D_i$$

# Effects of jitter

- Periodic events with jitter have an arrival time which may be early or late, within a bounded interval:
  - events arrive at  $t_0 + nT \pm J$
- Jitter may have a delay effect on lower priority tasks



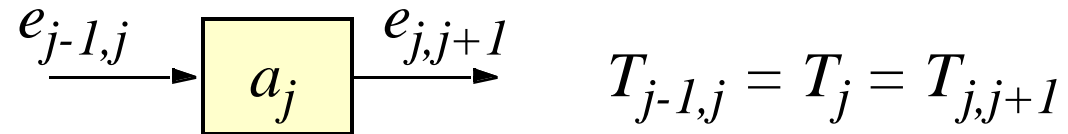
# More general approach



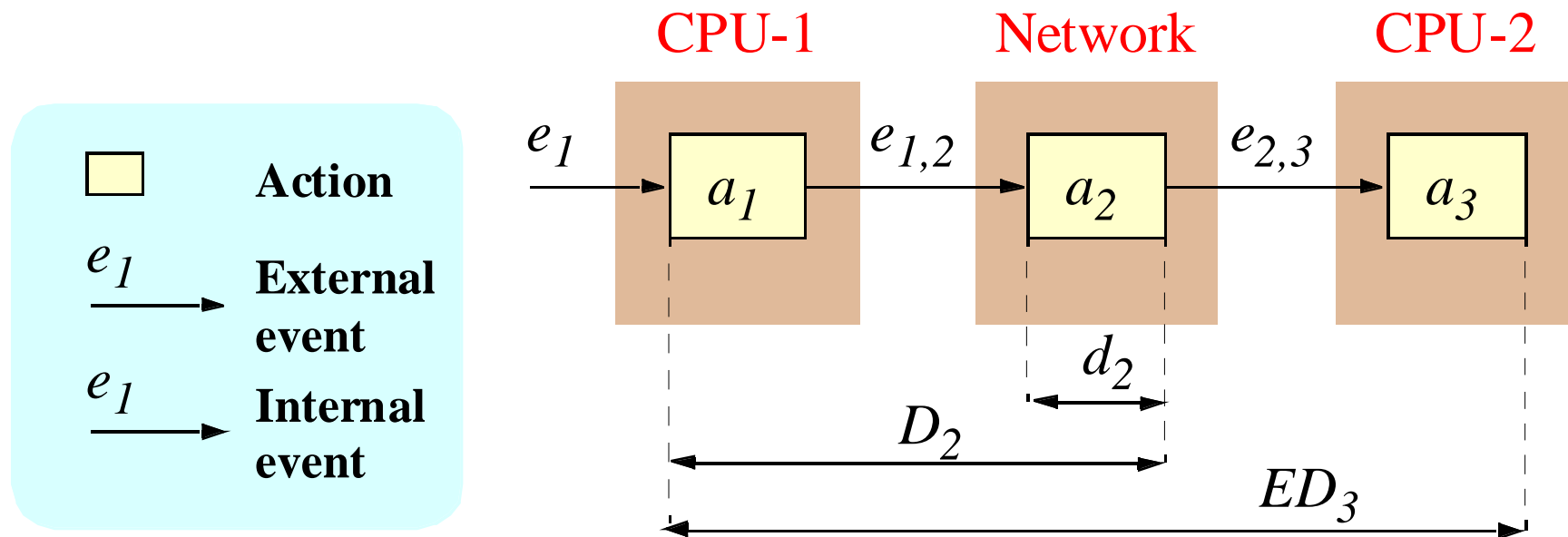
**Scenario(instance) based, distributed, control-flow dependencies**

# Distributed system model

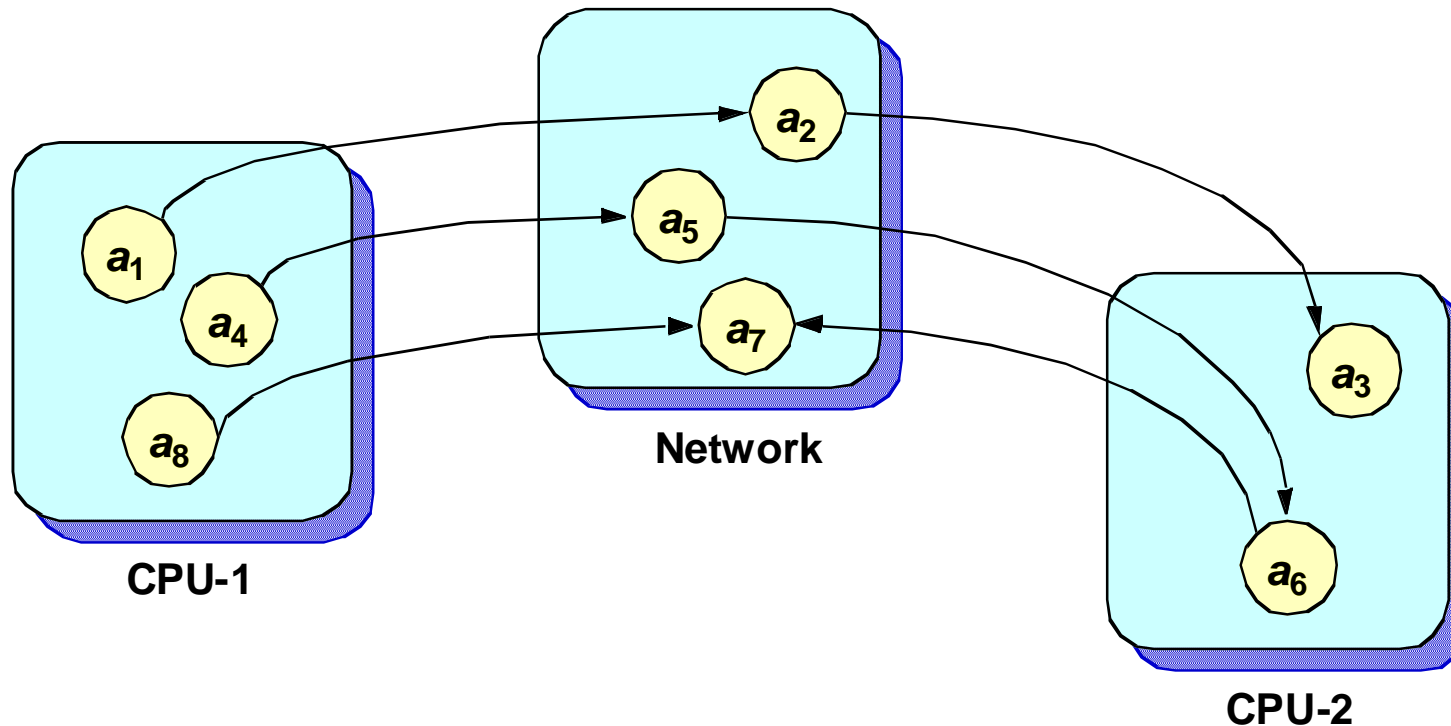
Linear Action:



Linear Response to an Event:

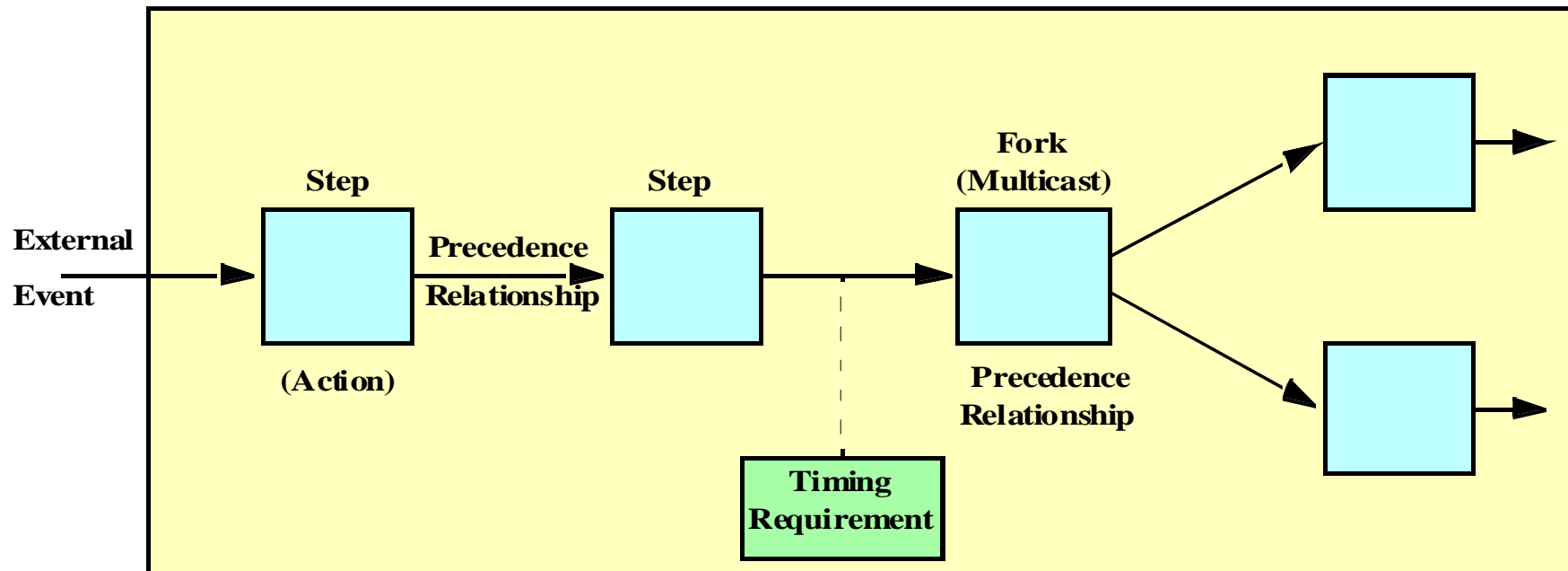


# Jitter in distributed systems

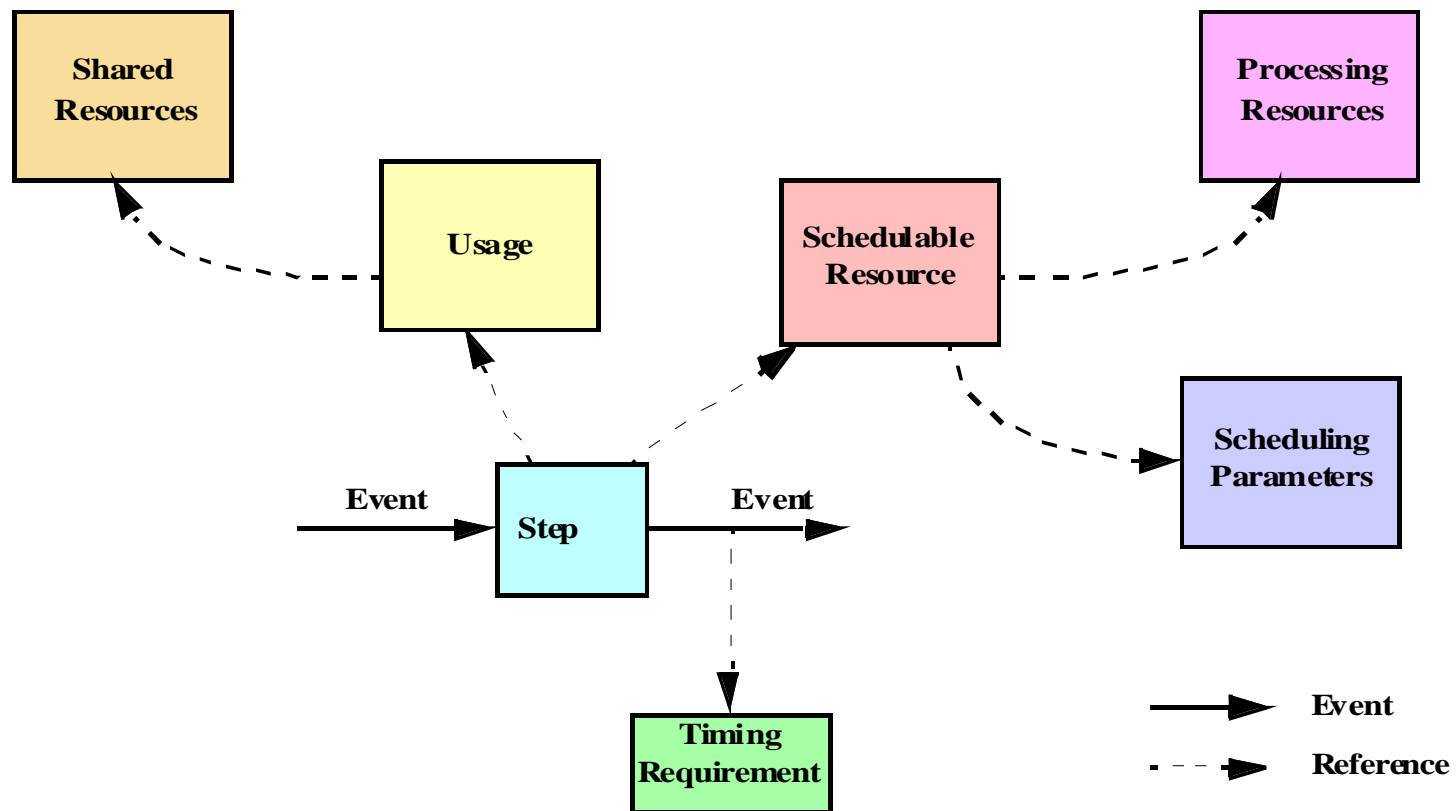


- ❑ Jitter in one processing resource depends on the response times in other processing resources
- ❑ Response times depend on jitters

# Transactions



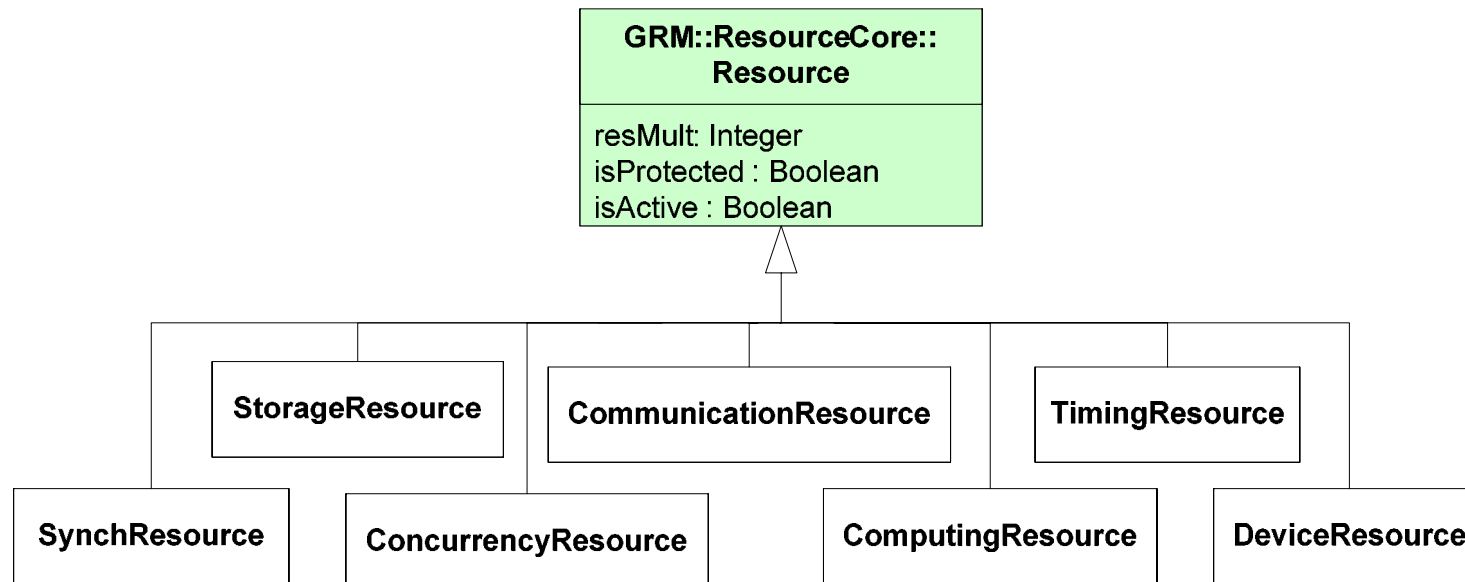
# Transactions

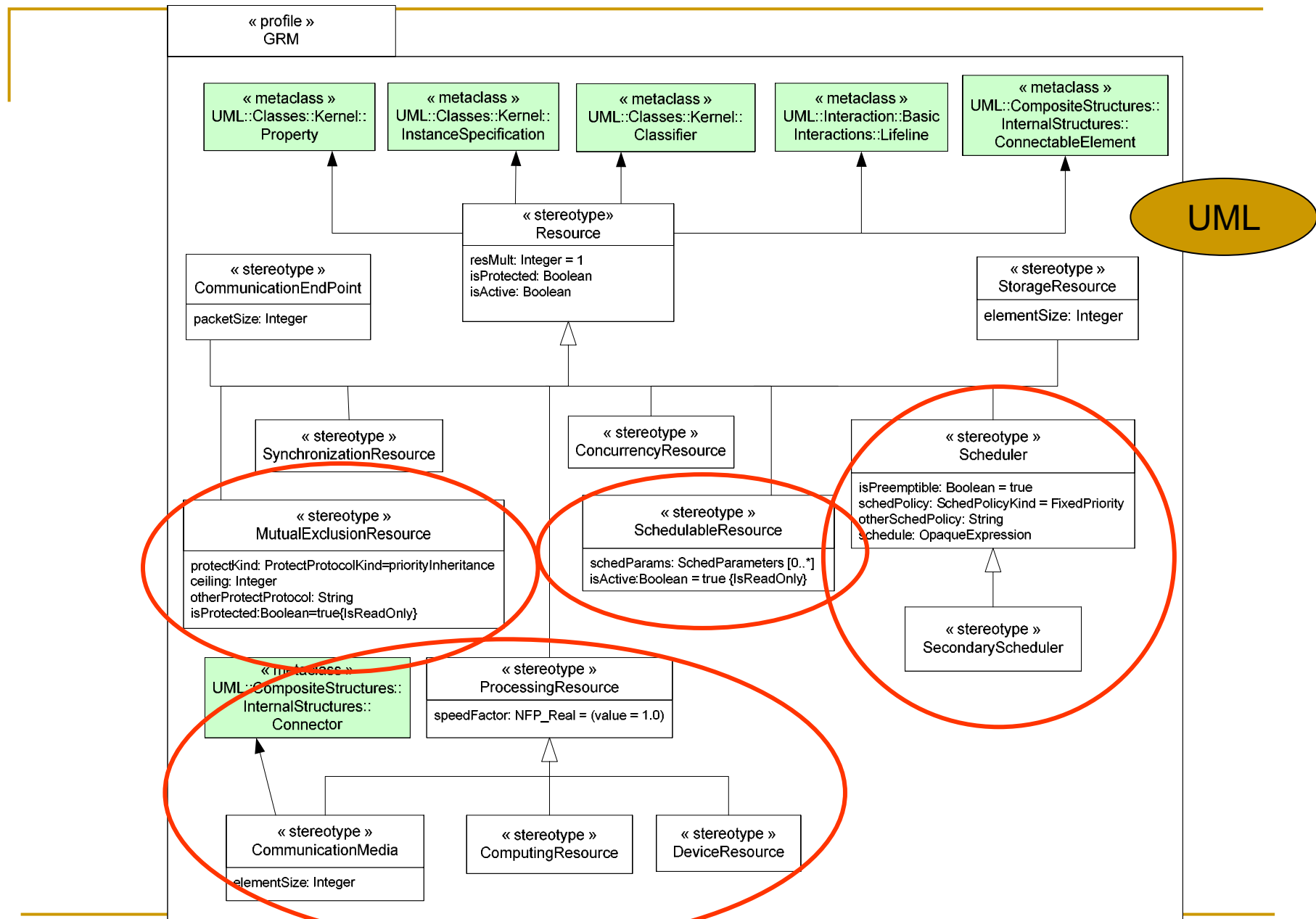




# Resources

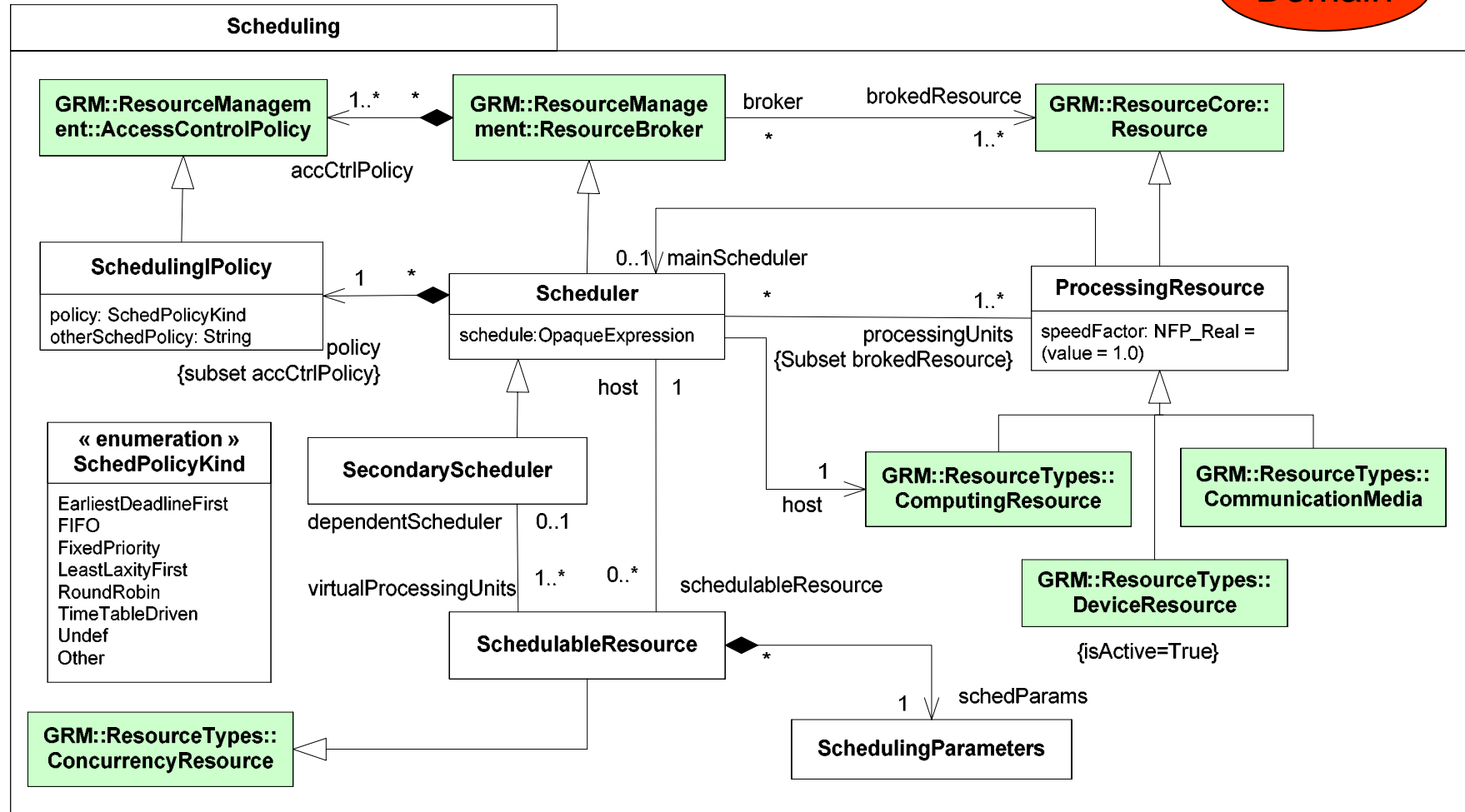
Domain





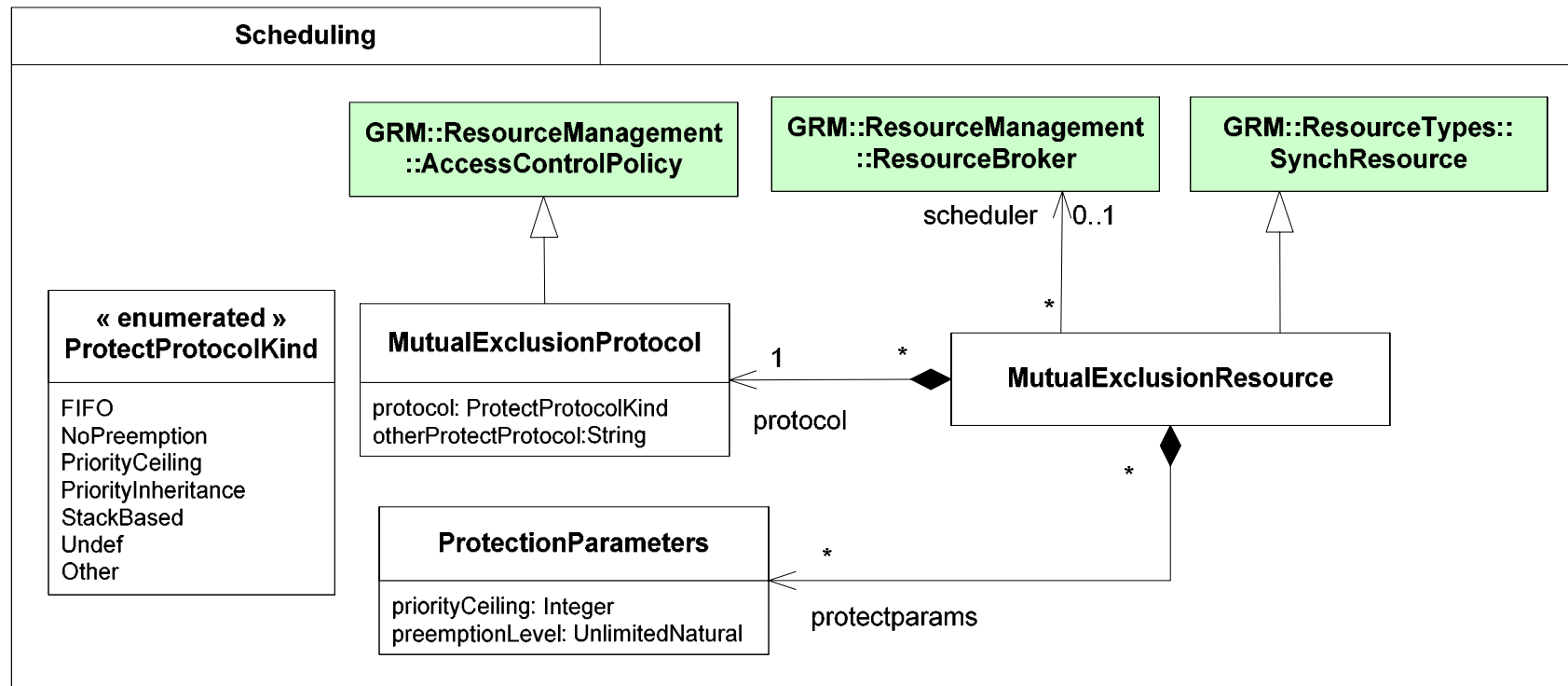
# Domain model for Schedulable resources

Domain



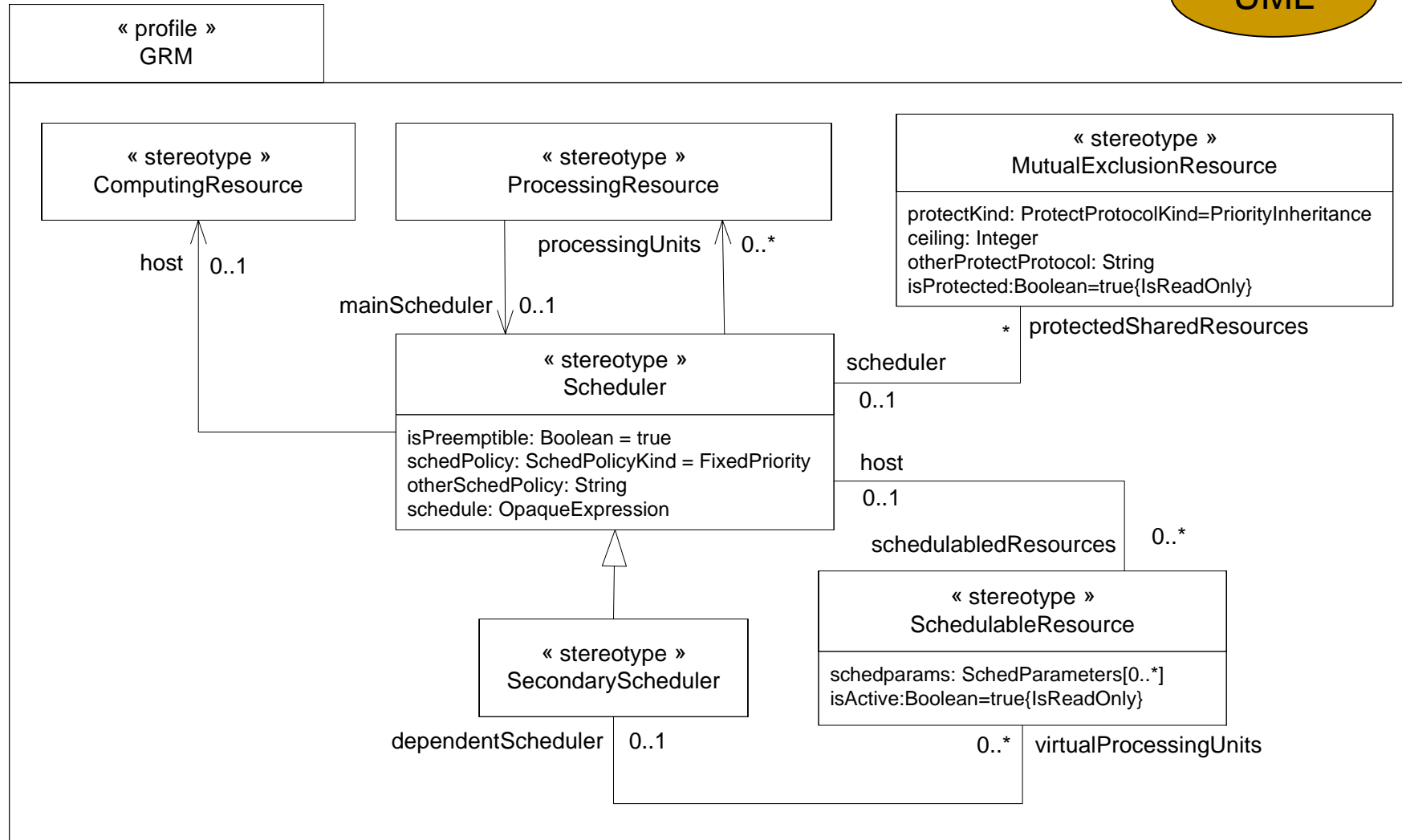
# Shared resources

Domain

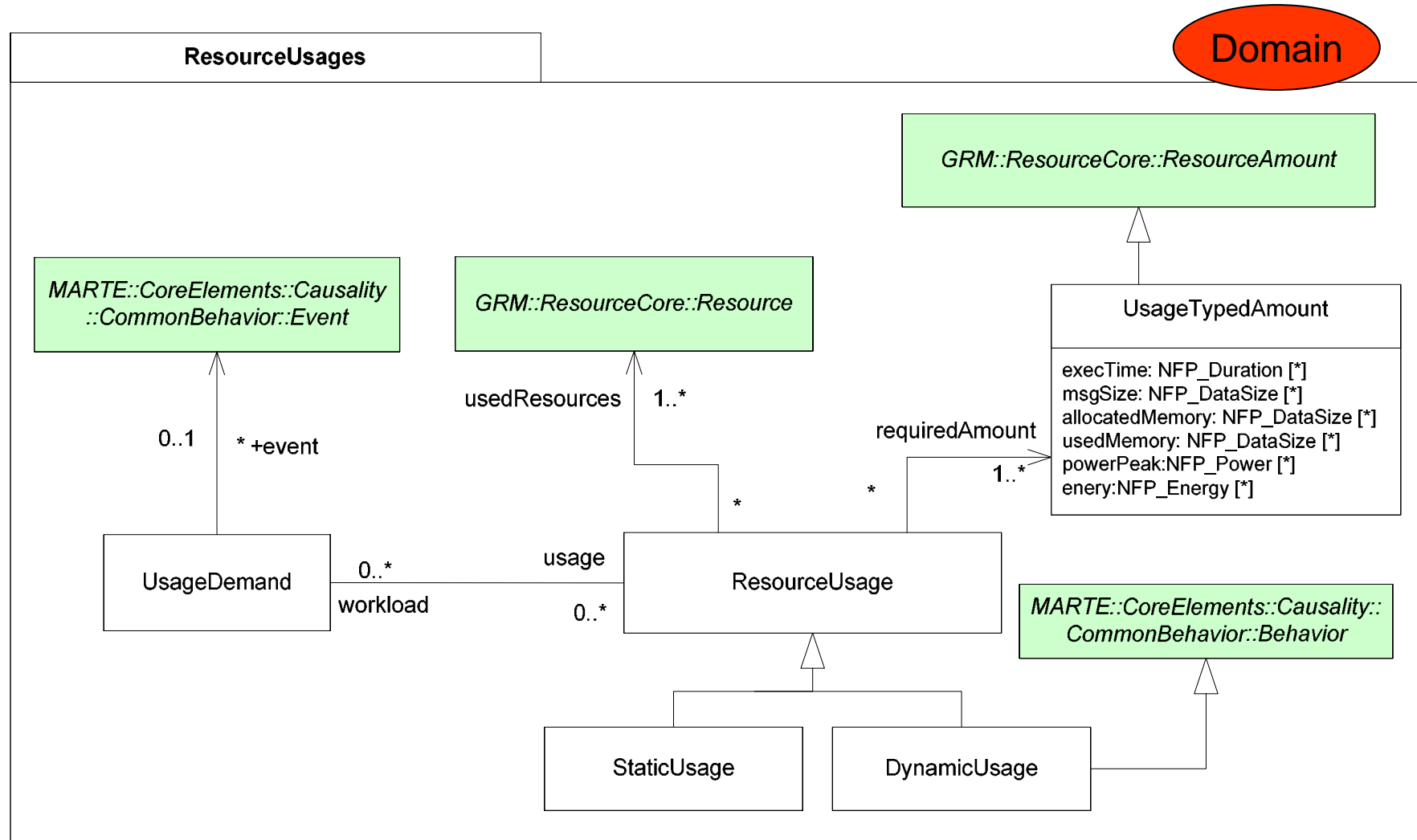


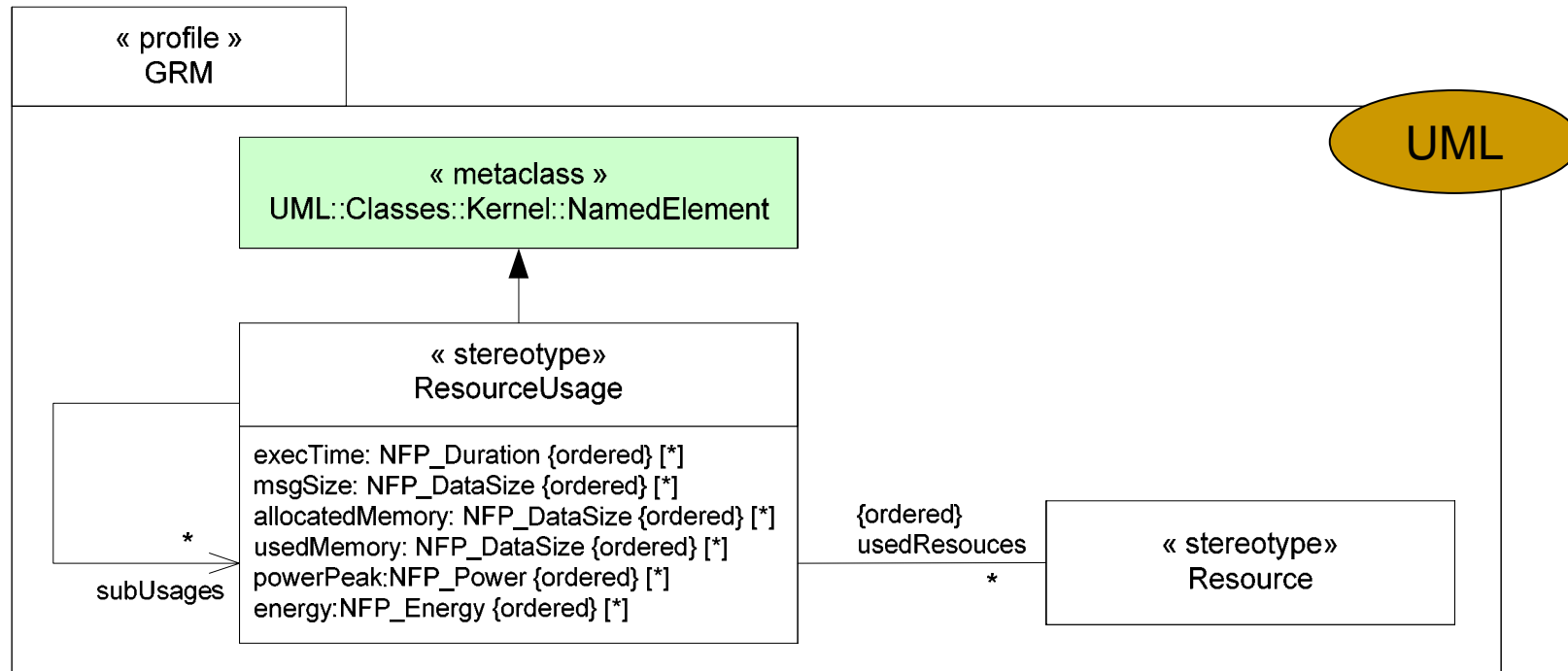
# Extensions for scheduling

UML



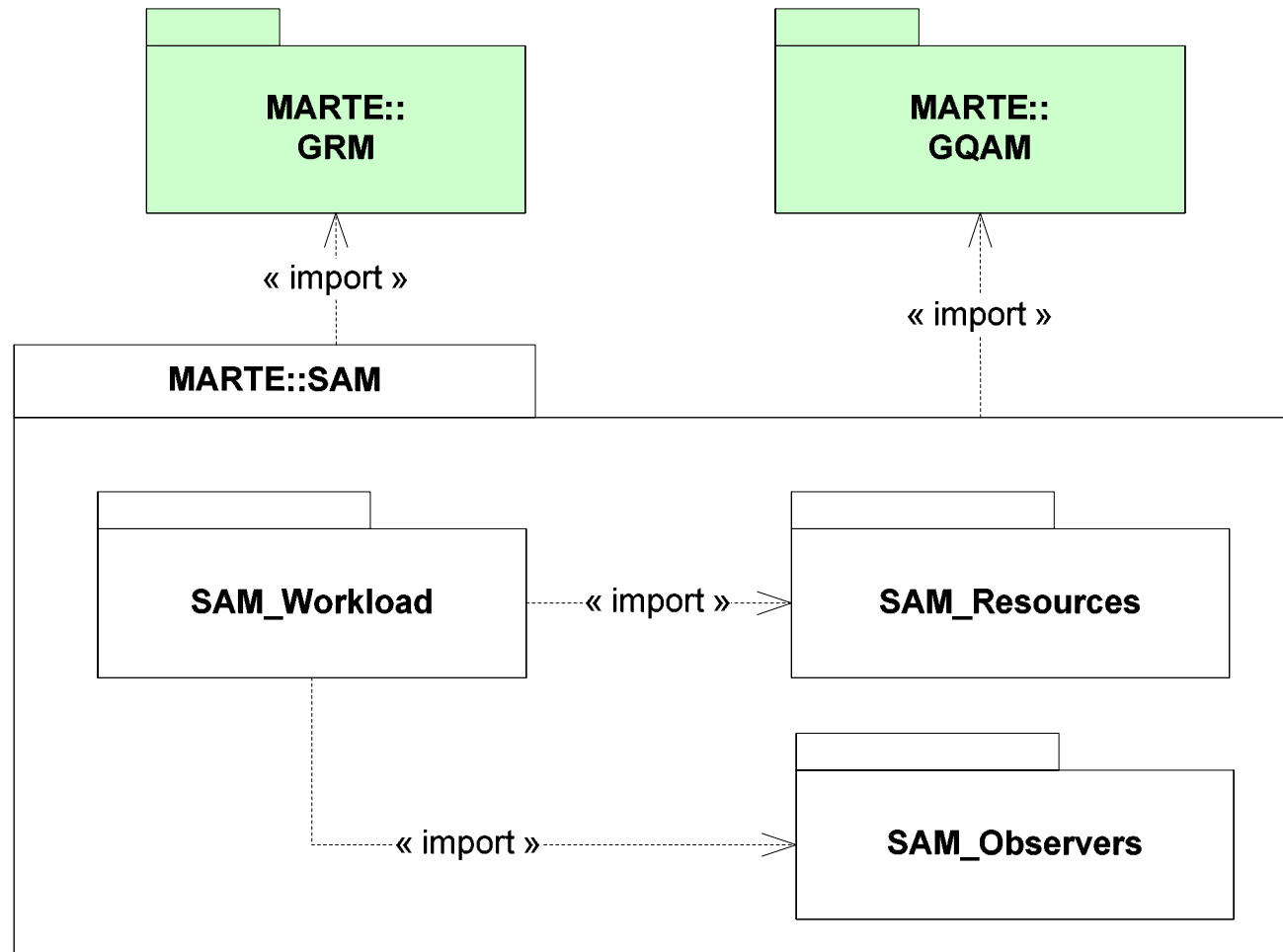
# Resource Usage



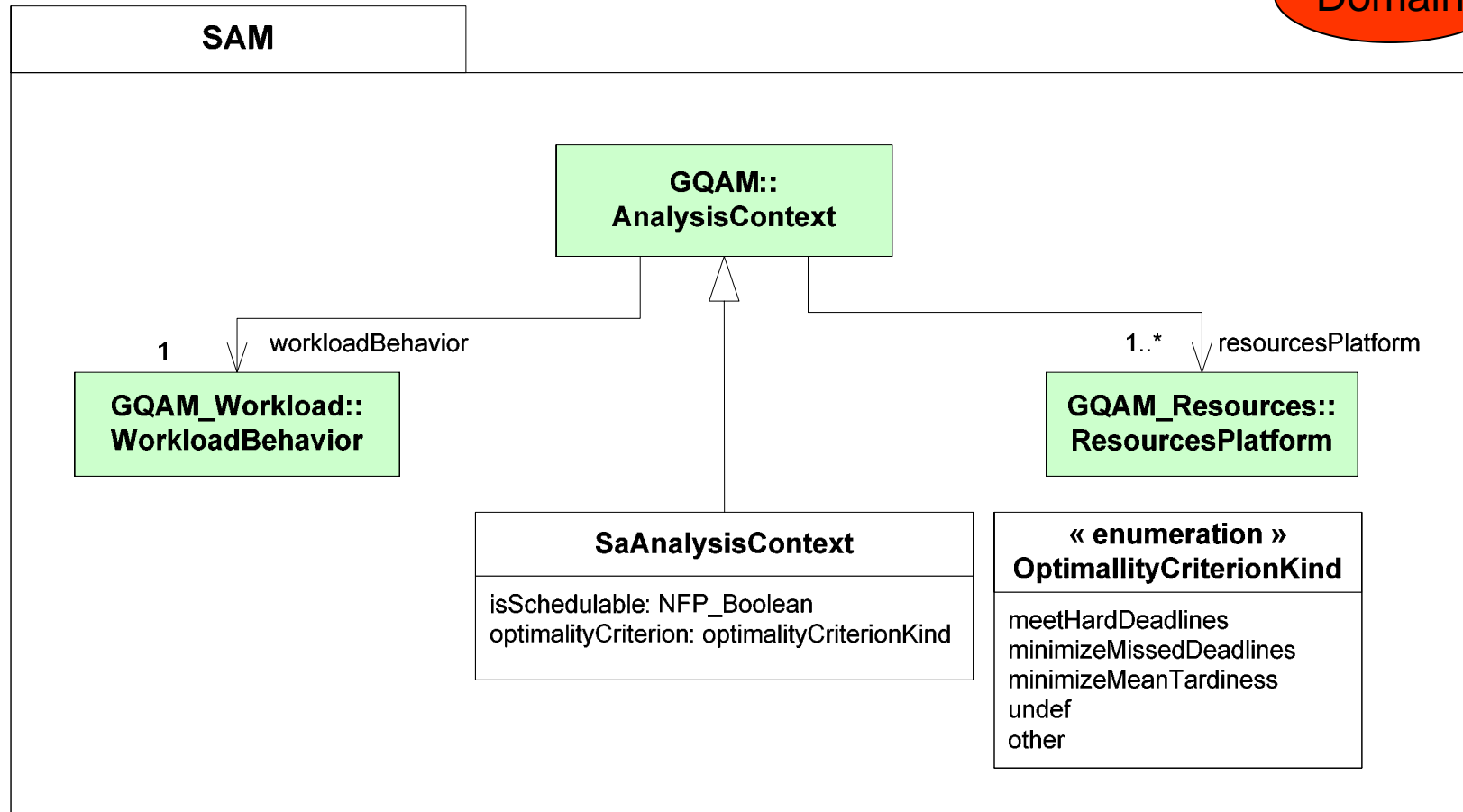


1. If the list usedResources is empty the list subUsages should not be empty and viceversa.
2. If the list usedResources has only one element, all the optional lists of attributes refer to this unique Resource and at least one of them must be present.
3. If the list usedResources has more than one element, all of the optional lists of attributes that are present, must have that number of elements, and they will be considered to match one to one.
4. If the list subUsages is not empty, and any of the optional lists of attributes is present, then more than one annotation for the same resource and kind of usage may be expressed. In this case, if the annotations have also the same source and statistical qualifiers they will be considered in conflict, and hence the ResourceUsage inconsistent.

# Structure of SAM

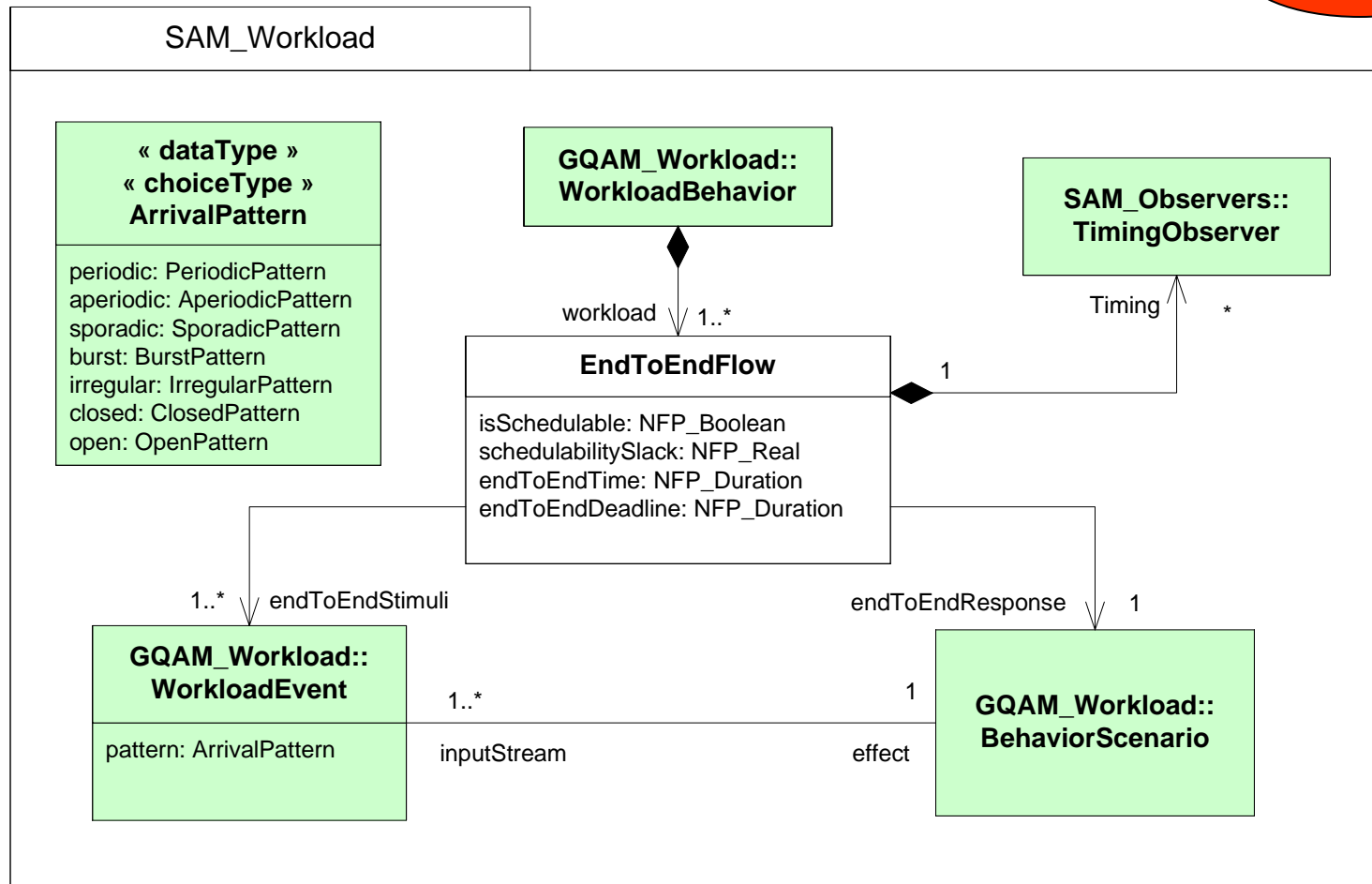




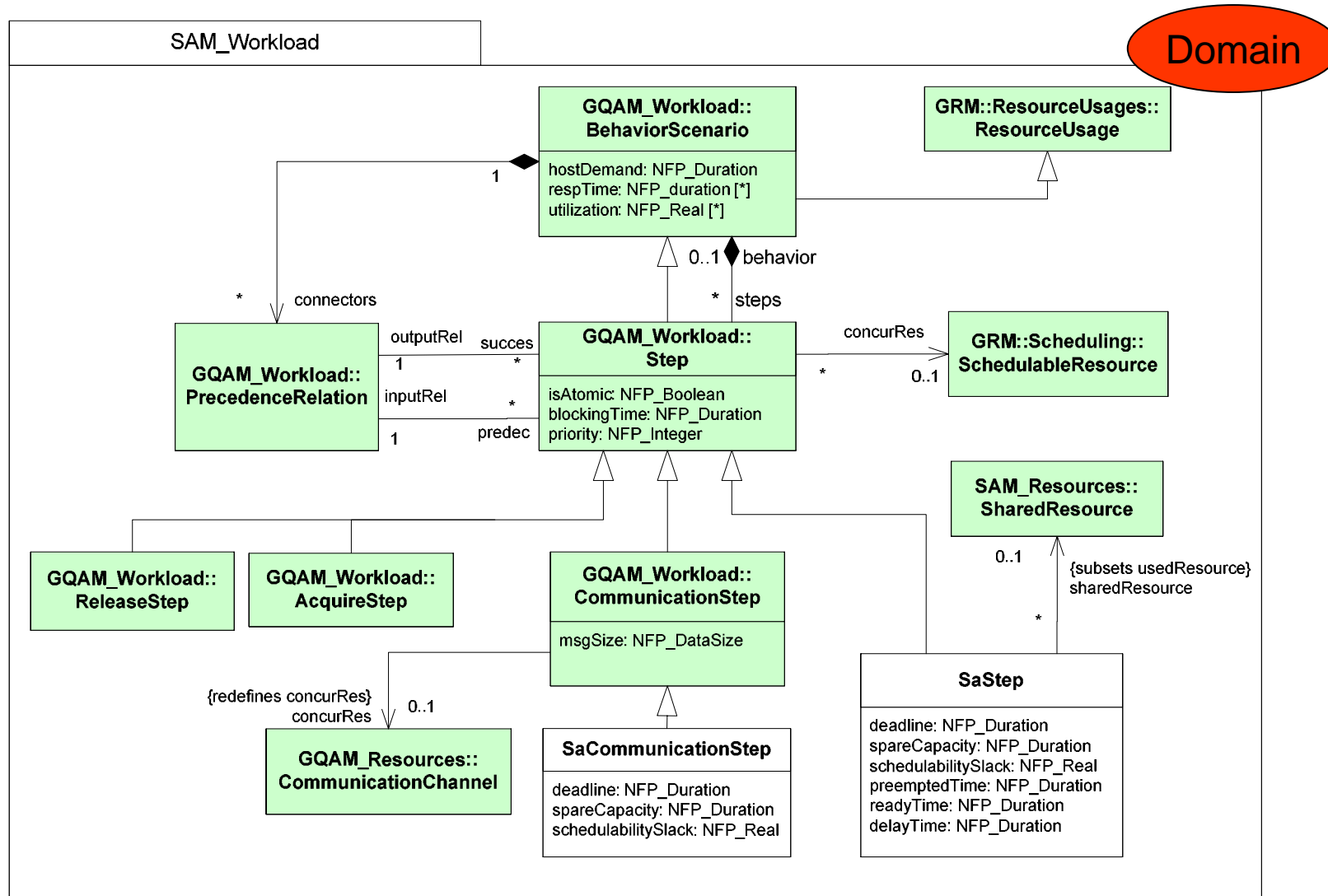


# SAM\_Workload: End-to-endFlow

Domain

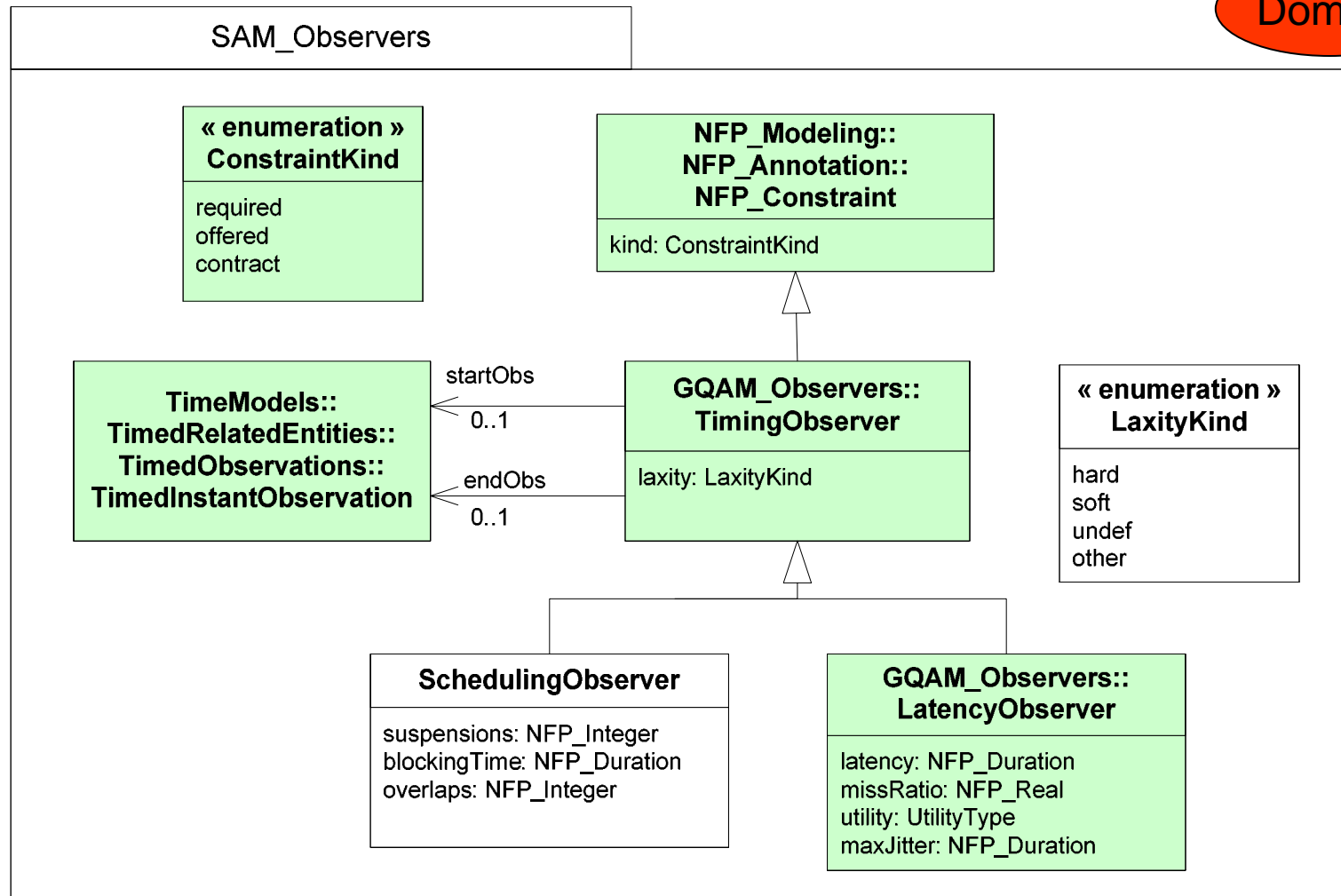


# SAM\_Workload: BehaviorScenario

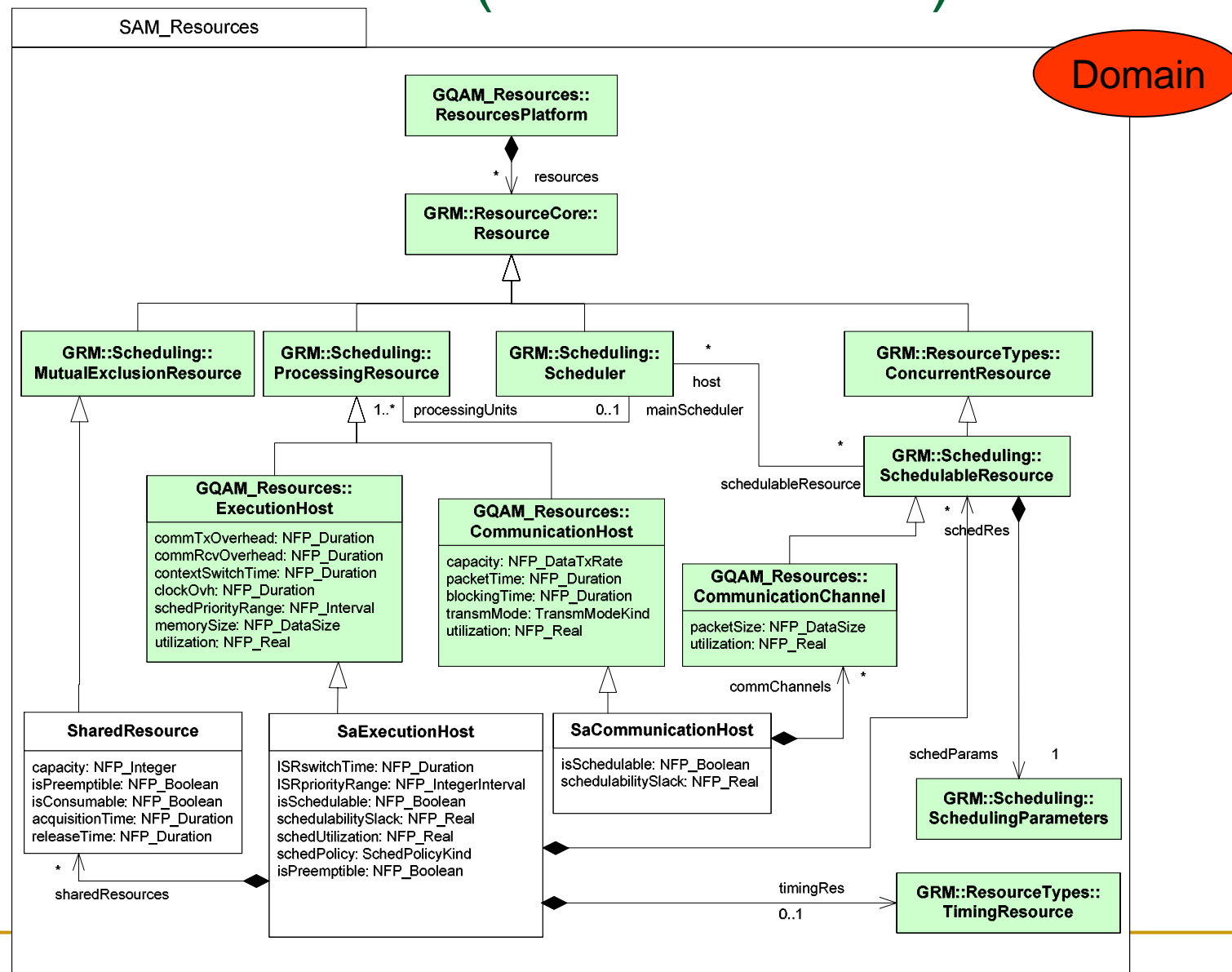


# SAM\_Observers

Domain

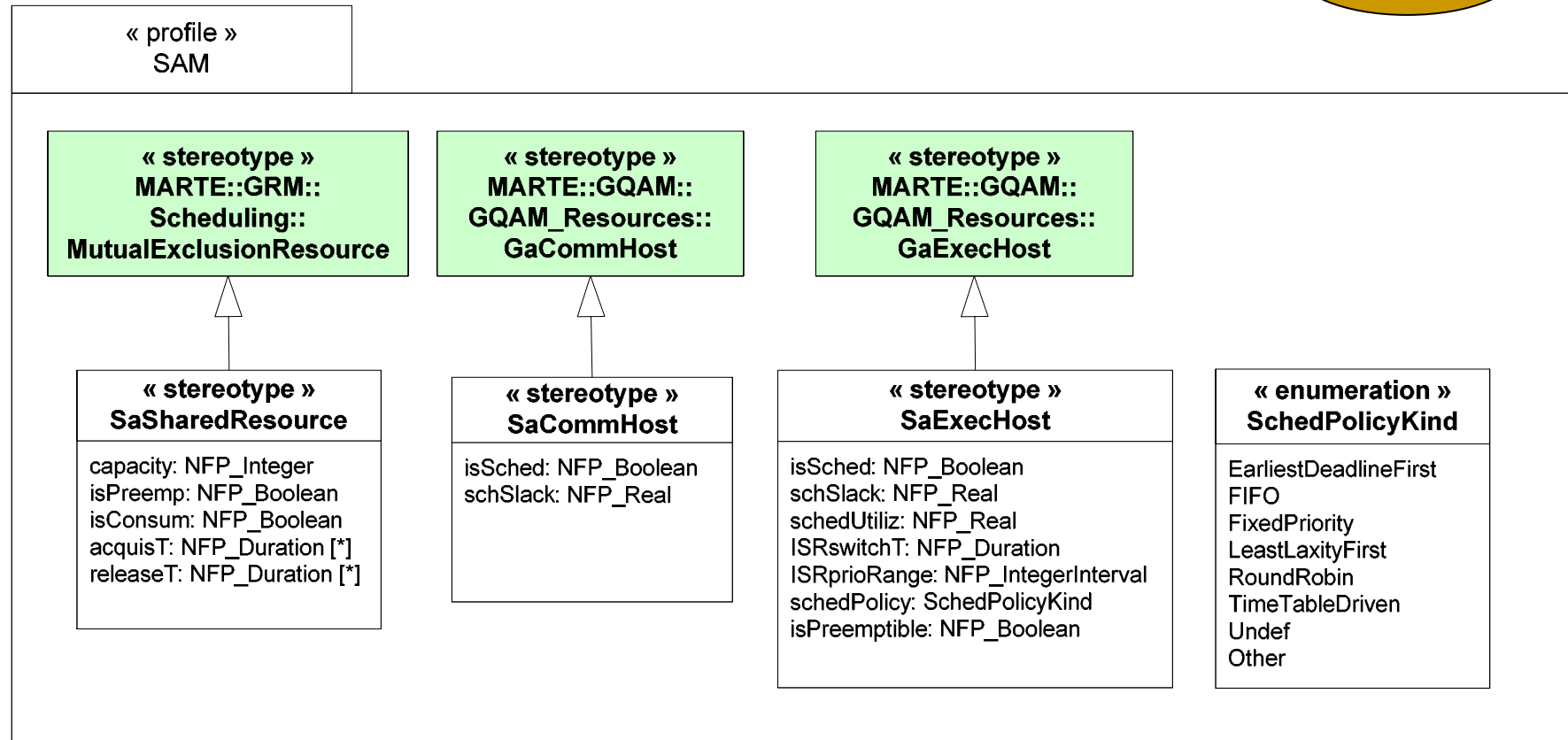


# Resources in SAM (domain model)

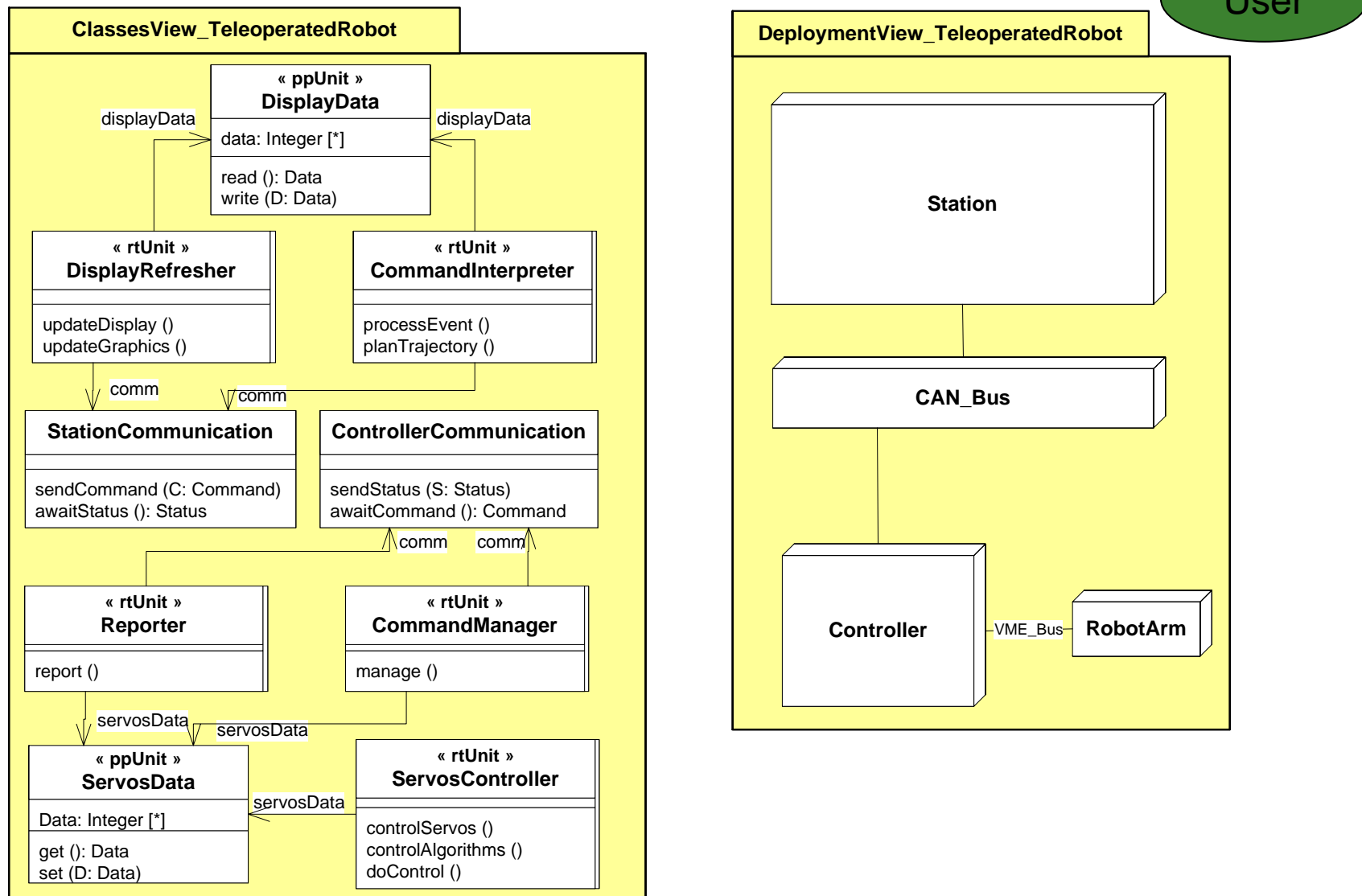


# Resources in SAM (profile)

UML

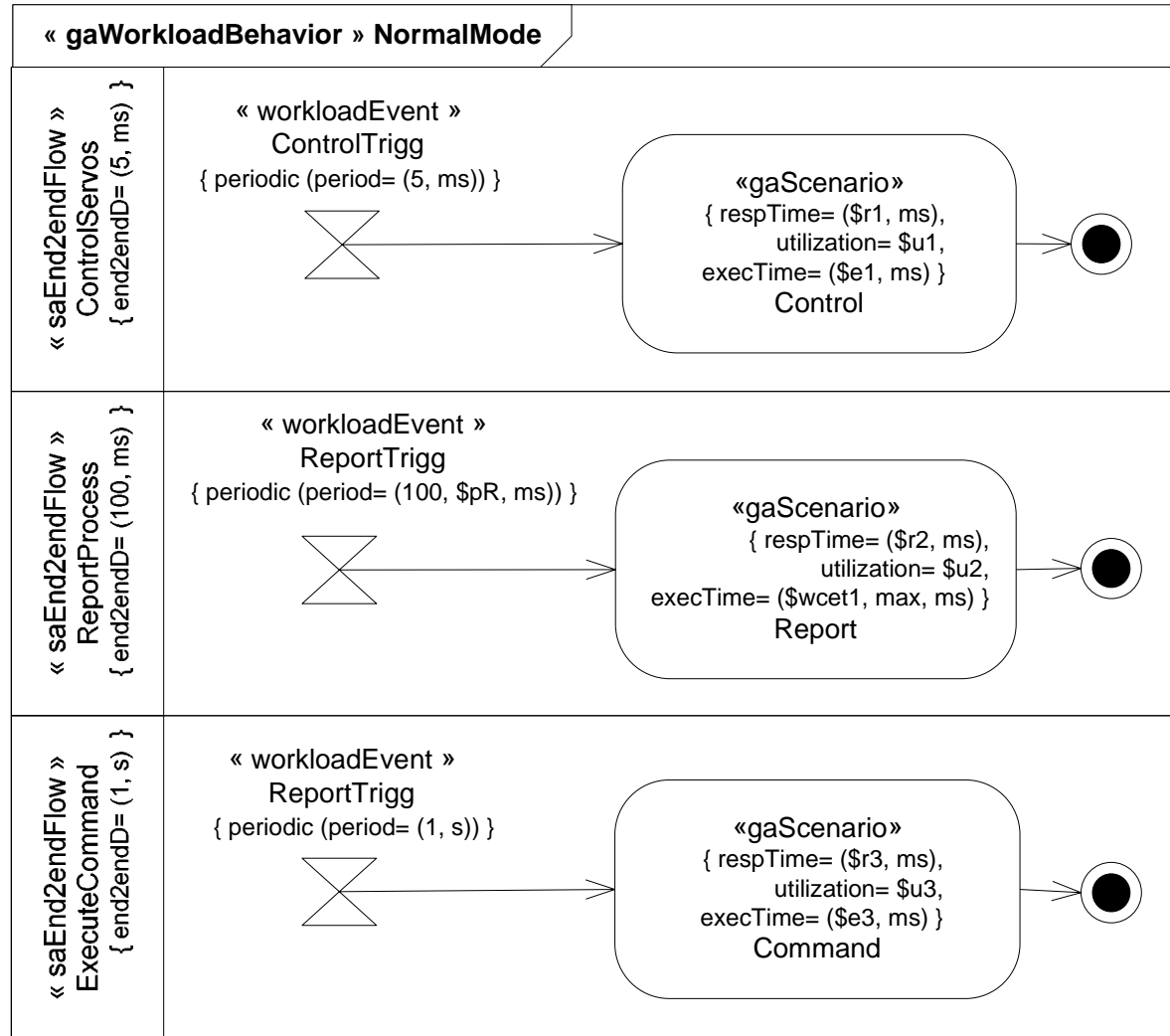


# Example



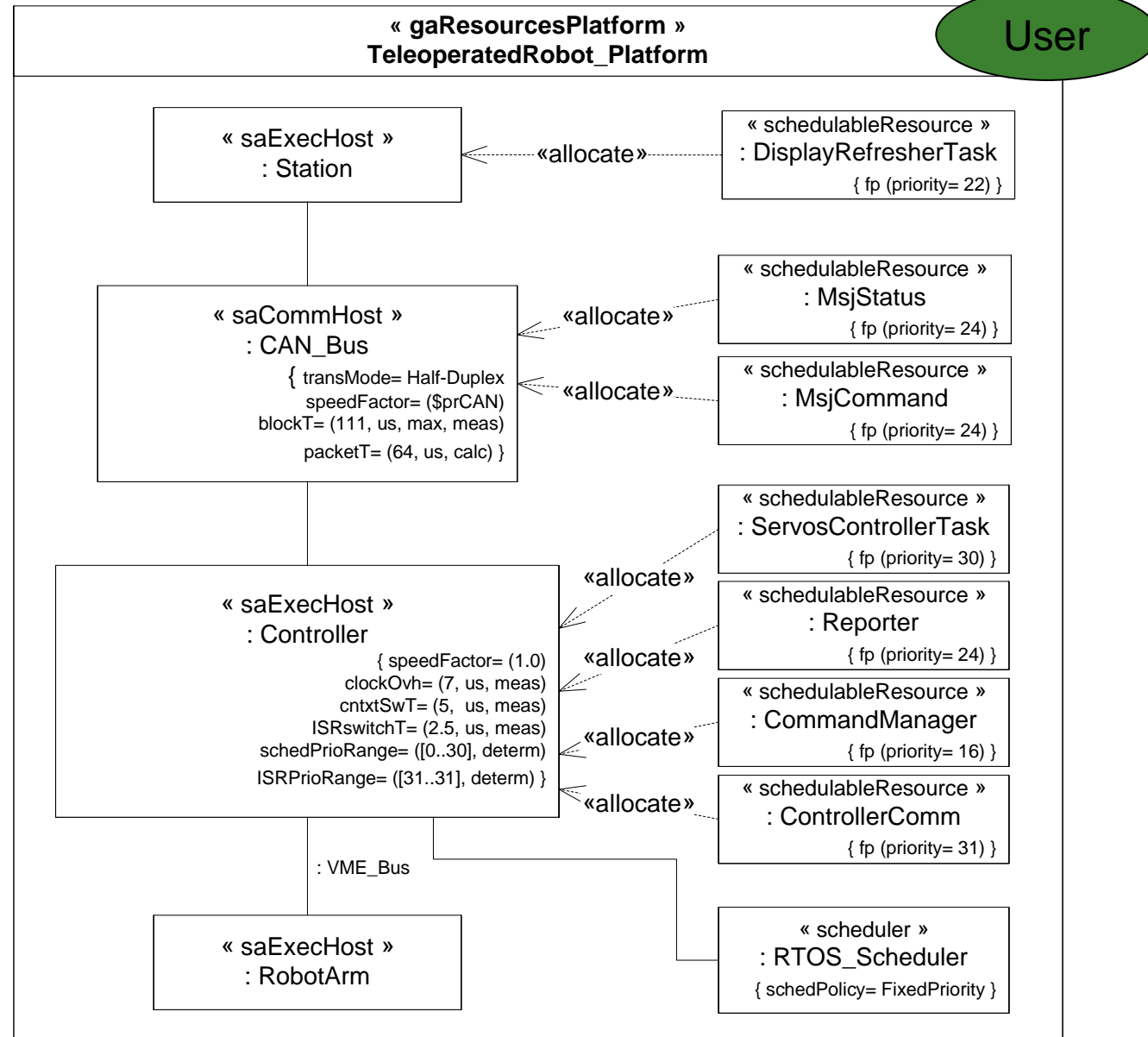
# Real-time situation

User



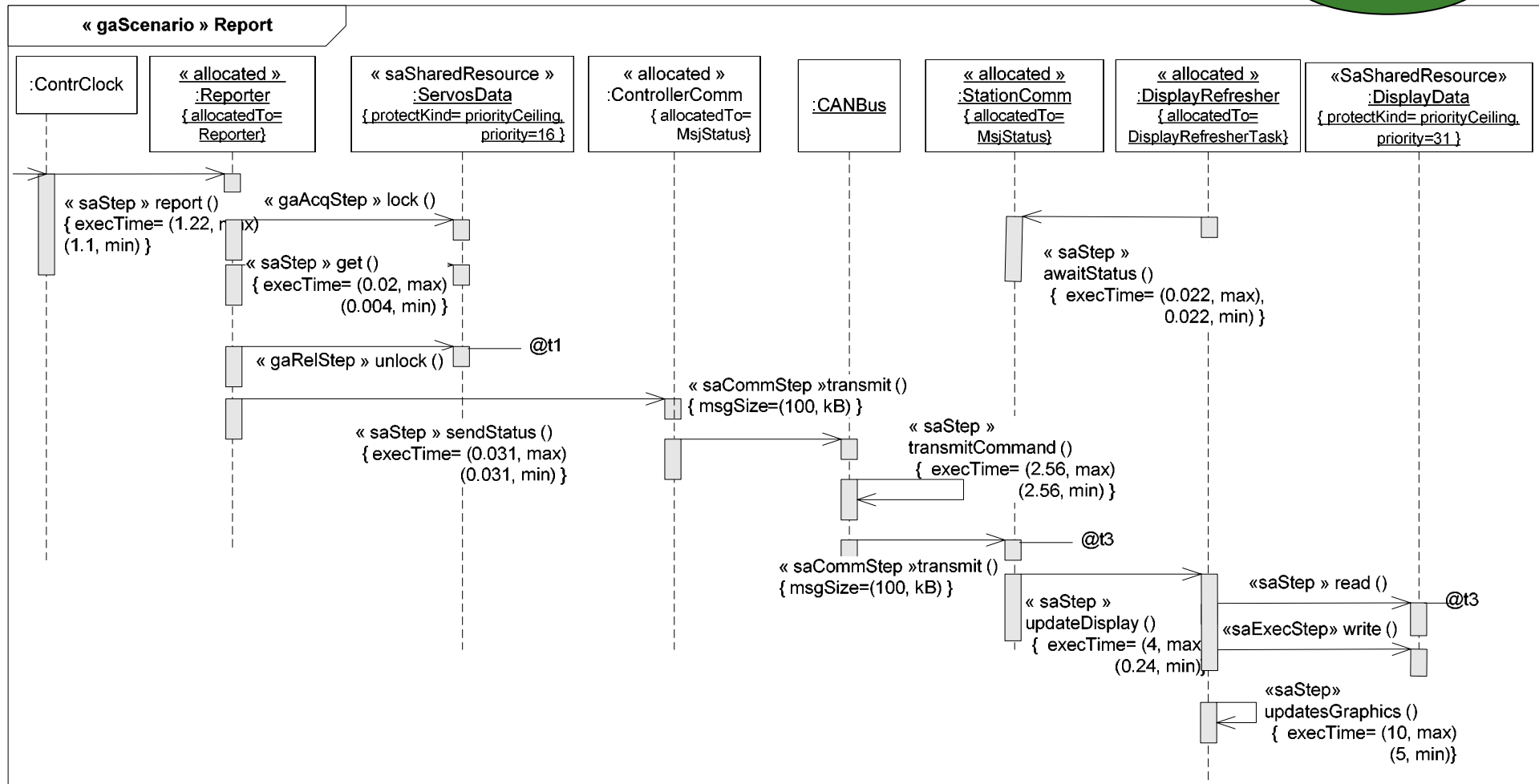


# Platform



# A behavior scenario

User



# Parametric analysis contexts

User

